Series-, Parallel-, and Inter-Connection of Solid-State Arbitrary Fractional-Order Capacitors: Theoretical Study and Experimental Verification

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\section*{ABSTRACT}
In this paper, general analytical formulas are introduced for the determination of equivalent impedance, magnitude, and phase, i.e., order, for \(n\) arbitrary fractional-order capacitors (FoCs) connected in series, parallel, and their interconnection. The approach presented helps to evaluate these relevant quantities in the fractional domain, since the order of each element has a significant effect on the impedance of each FoC and their equivalent capacitance cannot be considered. Three types of solid-state fractional-order passive capacitors of different orders, using ferroelectric polymer and reduced graphene oxide-percolated P(VDF-TrFE-CFE) composite structures, are fabricated and characterized. Using an impedance analyzer, the behavior of the devices was found to be stable in the frequency range 0.2 – 20 MHz, with a phase angle deviation of \(\pm 4^\circ\). Multiple numerical and experimental case studies are given, in particular for two and three connected FoCs. The fundamental issues of the measurement units of the FoCs connected in series and parallel are derived. A MATLAB open-access source code is given in the Appendix for easy calculation of the equivalent FoC magnitude and phase. The experimental results are in good agreement with the theoretical assumptions.

\section*{INDEX TERMS}
Arbitrary-order FoC, FoC, fractional calculus, fractional-order capacitor, interconnection, MATLAB open access source code, parallel connection, series connection, solid-state device.

\section*{I. INTRODUCTION}
Fractional-(non-integer)-order circuits and systems offer unique benefits by enabling broader impedance matching and allowing the tunability of frequency response of electronic circuits. So-called fractional-order capacitors (FoCs), known also as constant-phase elements, are crucial fundamental components of these circuits and are extensively used in a wide range of applications [1]–[26]. As is known, FoCs possess both real and imaginary impedance components since the fractional models need to account for both the order and frequency, while an ideal capacitor has only an imaginary component. In electrical engineering in particular, the constant-phase behavior of capacitors is explained as the frequency dispersion of the capacitance by dielectric relaxation, where the electric current density follows changes in the electric field with a delay. In 1994, to express this phenomenon of “off the shelf” real capacitors mathematically, the capacitance current in the time domain was given as [27]:

\[ i(t) = C \frac{d^\alpha u(t)}{dt^\alpha}, \quad 0 < \alpha < 1, \]  

where \(\frac{d^\alpha u(t)}{dt^\alpha}\) denotes the ‘fractional-order time derivative’. Now, the impedance of an ideal FoC can be derived from
in the frequency domain as:
\[ Z(s) = Ds^{-\alpha}. \]  

Here, \( \alpha \) is the order of the FoC, which is known as the dispersion coefficient, and \( D \) is the coefficient of pseudo-capacitance expressed in units of Farad \( \text{sec}^{\alpha-1} \). By substituting \( j\omega \) for \( s \), where \( j \) is a complex number and \( \omega \) is the radial frequency, in the frequency domain the impedance becomes:
\[ |Z(j\omega)| = |D(j\omega)^{-\alpha}| = |D\omega^{-\alpha} (\cos \varphi + j \sin \varphi)|, \]

where the phase is given in radians \( (\varphi = -\alpha \pi/2) \) or in degrees \( [\varphi = -90\alpha] \), while the argument in ohms \( \Omega \).

Accordingly, the magnitude depends on frequency versus \( \alpha \) and its value varies by 20\( \alpha \) dB per decade of frequency and its characteristic decreases. However, the argument \( \varphi \) of the impedance is constant and independent of the angular frequency \( \omega \).

The series and parallel connections of FoCs are the two elementary and most important structures that allow us to calculate the impedance of equivalent capacitance. They play a crucial role in investigating the dielectric properties of zinc flakes/flexible polyvinylidene fluoride (ZFs/PVDF) composites [28] and in practical applications such as modeling of supercapacitors [29] or designing of supercapacitor composites [28] and in practical applications such as modeling of supercapacitors [29] or designing of supercapacitor composites [28]. Since the order of each element in the connection has a significant effect on the impedance of each FoC, it cannot be neglected when calculating equivalent impedance. Bearing these ideas in mind, to the best of the authors’ knowledge, there are only a few studies that focus on the series and parallel connection of FoCs, mainly on the theoretical level only [31]–[34]. In [31], the equivalent impedance expressions of identical-order FoCs connected in series and parallel are given, but they were not proved by experiments due to lack of commercially available FoCs. Recently, the equivalent magnitude and phase formulas of two identical-order FoCs connected in series and in parallel have been verified experimentally by integrated operational transconductance amplifier (OTA)-based emulators [32]. Later on, in [33] and [34], Pu showed the possibility of connections between arbitrary orders of fractals. However, the theory that Pu introduced is based on positioning the purely ideal fractal in Chua’s circuit axiomatic element system and is not sufficient to calculate the equivalent magnitude and phase, i.e., order of FoCs, easily. Therefore, the main contributions of the present work are:

1) The general formulas for impedance, magnitude, and phase response of the series and parallel arbitrary-order \( n \) FoCs according to the main definition of the FoC are given as a complete study. Furthermore, the units of these physical dimensions are discussed.

2) Three types of fabricated ferroelectric polymer or reduced Graphene Oxide (rGO)-percolated P(VDF-TrFE-CFE) composite structure-based passive FoC devices of three different orders are described, together with their precise characterization including their pseudo-capacitances and bandwidth of operation. Note that this paper reports the first experimental verification of series- and parallel-connected FoCs fabricated solid-state passive FoCs, in contrast to using RC ladder structures [31] or active IC emulators [32].

3) Theoretical assumptions calculated via a newly developed MATLAB open access source code given in Appendix are proved by experimental verification using fabricated passive FoCs. Here it is important to underline that although the integer-order case (identical orders \( \alpha = 1 \)) is well-known as the core of the physical calculation, the fractional-order (arbitrary-orders) as a novel case also matches well with the assumptions and proves our novel core idea.

The rest of the paper is organized as follows: In Section II, the general formulas for impedance, magnitude, and phase responses of series- and parallel-connected \( n \) FoCs are derived. Fabrication process and experimental characterization of three types (orders 0.69 (TP2), 0.92 (P2), 0.62 (G2)) of solid-state compact and stable-in-phase (in the measured frequency range 0.2 MHz – 20 MHz) electric passive FoCs, in particular those based on an actual dielectric material, are explained in Section III A. The experimental results for two and three series-, parallel-, and inter-connected FoCs are presented in Section III B. A brief discussion of obtained results and final conclusions are given in Sections IV and V, respectively.

II. MATHEMATICAL DESCRIPTION OF \( n \) FoCs CONNECTION

This section aims to introduce general formulas that help to simplify \( n \) FoCs connected in a circuit. Hence, based on the proposed fractional-order measurement units and physical dimensions of FoC, the rules for in-series and parallel connected FoCs are presented.

A. ANALYSIS OF FRACTIONAL-ORDER CAPACITORS IN SERIES

In particular, having multiple FoCs in a circuit, the main aim is to replace them with a single equivalent capacitor and/or reach a desired phase angle with a combination of arbitrary-order capacitors. Therefore, considering the connection of \( n \) FoCs in series as shown in Fig. 1, the impedance of each FoC can be described as:
\[ Z_{a1}(s) = \frac{1}{s^{\alpha_{a1}}}, \quad Z_{a2}(s) = \frac{1}{s^{\alpha_{a2}}}, \quad Z_{a3}(s) = \frac{1}{s^{\alpha_{a3}}}, \ldots, \quad Z_{an}(s) = \frac{1}{s^{\alpha_{an}}}. \]

Assuming the voltage across it is \( v_{a1} + v_{a2} + v_{a3} + \ldots + v_{an} = v_{eq,s} \) and the current flowing through is \( i_{a1} = i_{a2} = i_{a3} = \ldots = i_{an} = i_{eq,s} \), the equivalent total impedance \( Z_{eq,s} \), and its unit is...
derived as:

\[ Z_{eq,s}(s) = Z_{a_1} + Z_{a_2} + Z_{a_3} + \ldots + Z_{a_n} = \sum_{i=1}^{n} \frac{1}{s^\alpha C_{a_i}} \]  \[ (4) \]

By using Euler’s identity \( s = j\omega \), where \( j = e^{j\pi/2} \), and substituting in (4), the general formulas for equivalent magnitude and phase responses of \( n \) FoCs connected in series are expressed as (5) and (6), respectively, where the indexes from \( i \) to \( k \) are the numbers of FoCs, each counted from 1 to \( n \). The function of the sum is valid under the condition that \( i < j < \ldots < l \) and \( k \neq i, j, \ldots, l \). Note that the derived orders of FoCs affect the power of angular frequency and also the degree of the cosine in magnitude function (5).

Furthermore, the phase of the equivalent FoC is dependent on the angular frequency \( \omega \). From (6) it is evident that the angle with the positive x-axis is decreasing while the sum of orders is increasing. The phase of FoC must be between \( 0 < \text{Arg}[Z_{eq,s}(s)] < -\pi/2 \).

Now, let us consider Case I, when FoCs have different pseudo-capacitances, i.e. \( C_{a_1} \neq C_{a_2} \neq C_{a_3} \neq \ldots \neq C_{a_n} \), while assuming their orders are identical \( \alpha_1 = \alpha_2 = \alpha_3 = \ldots = \alpha_n = \alpha \) (\( \alpha \in (0, 1) \)). Then the impedance, magnitude, and phase formulas in (4)–(6) turn out to be as given in Table 1, where in (7) and (8) \( \sum_{i=1}^{n} \frac{1}{C_{a_i}} = \frac{1}{s^n C_{eq,s}} \). The given case study is straightforward since each capacitor will be experiencing the same current and the voltage across each FoC will increase with respect to this current. Thus, the total voltage across all capacitors will increase at a greater rate than the voltage across individual capacitors.

On the other hand, in Case II, when considering the same pseudo-capacitances of FoCs, i.e. assuming \( C_{a_1} = C_{a_2} = C_{a_3} = \ldots = C_{a_n} = C_a \) with identical orders, the impedance, magnitude, and phase in (4)–(6) respectively become

\[ Z_{eq,e}(s) = \prod_{i=1}^{n} \frac{\omega^{\alpha_i} C_a}{j\omega^{\alpha_i}} \]  \[ (10) \]

\[ \text{Arg}[Z_{eq,e}(s)] = \tan^{-1} \left\{ \frac{\sum_{i,j \ldots, k=1}^{n} \omega^{\alpha_i + \alpha_j + \ldots + \alpha_k} \frac{1}{C_{a_k}} \sin \left( \alpha_i + \alpha_j + \ldots + \alpha_k \right) \pi / 2}{\sum_{i,j \ldots, k=1}^{n} \omega^{\alpha_i + \alpha_j + \ldots + \alpha_k} \frac{1}{C_{a_k}} \cos \left( \alpha_i + \alpha_j + \ldots + \alpha_k \right) \pi / 2} - \frac{n}{\sum_{i=1}^{n} (\alpha_i) \pi / 2} \right\} \]  \[ (12) \]

\( \alpha_i \neq \alpha_j \neq \ldots \neq \alpha_k \) \( i \neq j \neq \ldots \neq k \)

\( n \) of orders is increasing. The phase of FoC must be between \( 0 < \text{Arg}[Z_{eq,e}(s)] < -\pi/2 \).

Now, let us consider Case I, when FoCs have different pseudo-capacitances, i.e. \( C_{a_1} \neq C_{a_2} \neq C_{a_3} \neq \ldots \neq C_{a_n} \), while assuming their orders are identical \( \alpha_1 = \alpha_2 = \alpha_3 = \ldots = \alpha_n = \alpha \) (\( \alpha \in (0, 1) \)). Then the impedance, magnitude, and phase formulas in (4)–(6) turn out to be as given in Table 1, where in (7) and (8) \( \sum_{i=1}^{n} \frac{1}{C_{a_i}} = \frac{1}{s^n C_{eq,s}} \). The given case study is straightforward since each capacitor will be experiencing the same current and the voltage across each FoC will increase with respect to this current. Thus, the total voltage across all capacitors will increase at a greater rate than the voltage across individual capacitors.

On the other hand, in Case II, when considering the same pseudo-capacitances of FoCs, i.e. assuming \( C_{a_1} = C_{a_2} = C_{a_3} = \ldots = C_{a_n} = C_a \) with identical orders, the impedance, magnitude, and phase in (4)–(6) respectively become
each capacitor must be the same and let us label it as $v_{eq.p}$. In addition, applying Kirchhoff’s Current Law, the sum of currents in nodes will be $i + i_2 + i_3 + \ldots + i_n = i_{eq.p}$. Hence, by substituting the current flowing through each capacitor in the time-domain and transforming it to the Laplace domain, the equivalent total impedance $Z_{eq.p}$ of $n$ arbitrary-order FoCs connected in parallel can be expressed as:

$$\frac{1}{Z_{eq.p}(s)} = \frac{1}{Z_{a1}} + \frac{1}{Z_{a2}} + \frac{1}{Z_{a3}} + \ldots + \frac{1}{Z_{an}},$$

(13)

$$Z_{eq.p}(s) = \frac{1}{\alpha_1 C_{a1} + \alpha_2 C_{a2} + \alpha_3 C_{a3} + \ldots + \alpha_n C_{an}} = \frac{1}{\sum_{i=1}^{n} \alpha_i C_{ai}} \quad \text{[Ω]}$$

(14)

Thus, by substituting in (14) $s = j \omega$, while $j = e^{i\pi/2}$, the expressions for magnitude and phase are as follows:

$$|Z_{eq.p}(s)| = \left[ \frac{1}{\sum_{i=1}^{n} (\alpha_i)^2 C_{ai}^2 + 2 \sum_{i=1}^{n} \sum_{j=i+1}^{n} \alpha_i \alpha_j C_{ai} C_{aj} \cos(\alpha_i - \alpha_j) \frac{\pi}{2}} \right]^{1/2},$$

(15)

$$\text{Arg}[Z_{eq.p}(s)] = -\tan^{-1}\left[ \frac{\sum_{i=1}^{n} \alpha_i C_{ai} \sin\left(\frac{\alpha_i \pi}{2}\right)}{\sum_{i=1}^{n} \alpha_i C_{ai} \cos\left(\frac{\alpha_i \pi}{2}\right)} \right].$$

(16)

Again, when considering Case III, where FoCs have identical orders $\alpha_1 = \alpha_2 = \alpha_3 = \ldots = \alpha_n = \alpha$ ($\alpha \in (0, 1]$) but different pseudo-capacitances, i.e. $C_{a1} \neq C_{a2} \neq C_{a3} \neq \ldots \neq C_{an}$, then the impedance, magnitude, and phase responses are derived as (17)–(19) of Table 2, where in (17) and (18) $\sum_{i=1}^{n} C_{ai} = C_{eq.p}$. On the other hand (Case IV), if $\alpha_1 = \alpha_2 = \alpha_3 = \ldots = \alpha_n = \alpha$ ($\alpha \in (0, 1]$) and $C_{a1} = C_{a2} = C_{a3} = \ldots = C_{an} = C_a$, then (17)–(19) turn out to be (20)–(22).

Similar to Cases I and II of the arbitrary FoCs connected in series, the equivalent order of the resulting network is frequency-dependent and given by (16), but if the orders are identical, then it is frequency-independent as shown in (19) and (22) of Table 2. However, when $n$ identical-order FoCs are connected in parallel, the total equivalent impedance, magnitude, and phase responses are as simple as (20)–(22). It is worth noting that these relations are similar to integer-order capacitors connected in parallel. Hence, by increasing the number of capacitors, the equivalent magnitude may decrease and equivalent pseudo-capacitance may increase. In addition, the frequency and number of capacitors influence only the magnitude, while the order affects both the magnitude and phase responses. Units of impedance, magnitude, and phase responses of FoCs remain in both the series and parallel cases the same as in the integer-order case, i.e. the impedance and magnitude are in units of ohms and the phase in units of degrees, respectively.

### III. EXPERIMENTAL VERIFICATION

#### A. BRIEF DISCUSSION ON AVAILABLE FoCs AND NEW DEVICES

Despite the significant demand for and importance of fractional-order capacitors in many applications listed above, the lack of commercial availability of FoCs and restricted realizations of their emulators discussed below limit the researchers in performing experimental tests and consequently the growth of this research area. In particular, only few works in open access literature report practical implementations of FoCs [32], [35]–[40]. The first attempt was based on the fabrication of an infinite ladder network [35], which later on continued by developing fractal structures on silicon [36], lithium ions on the rough surface of metal electrodes [37], using the electrolytic process [38], by dipping a capacitive-type polymer-coated probe in polarizable medium [39], or emulating the FoC behavior with OTA as an active building block [32], [40]. In general, however, the FoCs produced using these techniques operate properly only in a limited low frequency range of up to 1 MHz; they are bulky and not easy to reproduce with required specifications, or the phase error is higher than ±5 degrees. Therefore, the phase response of FoC is not sufficiently stable and in specific frequency ranges significant ripples occur.

In this paper, three types of FoCs of different phase angles are fabricated [41], [42]. First, the PVDF and PVDF-TrFE-CFE powders are dissolved in a solvent, N, N-Dimethylformamide (DMF) separately in different vials (one vial for PVDF and three vials for PVDF-TrFE-CFE), under constant stirring at room temperature for two days to obtain 0.1 mg/ml polymer solutions. An rGO is weighed with the desired weight percentage, suspended in 1 ml DMF, and dispersed via ultrasonication for 1 hour. Later, dispersed rGO
TABLE 3. Measurement results of fabricated fractional-order capacitors (Note: * at $f_c = 2$ MHz).

| Capacitor No. | Device Type | Device Pin No. | $|Z|^*$ [kΩ] | $\varphi^*$ [Degree] | Order $\alpha^*$ | Pseudo-Capacitance $^*$ [Farad $ \cdot $ sec$^{\alpha-1}$] | Equivalent Capacitance $^*$ [F] | Phase Angle Deviation in Range (0.2–20) MHz [Degree] | Fabrication Technology |
|---------------|-------------|----------------|-------------|----------------------|------------------|---------------------------------|-----------------------------|--------------------------------|-----------------------|
| $C_{\alpha_1}$ | TP2         | 1              | 2.37        | -61.91              | 0.69             | 5.52 n                          | 33.6 p                      | ±4.01                        | Polymer dielectric        |
| $C_{\alpha_2}$ | TP2         | 4              | 2.24        | -61.68              | 0.69             | 5.89 n                          | 35.54 p                     | ±3.84                        | Polymer dielectric        |
| $C_{\alpha_3}$ | P2          | 9              | 6.09        | -82.63              | 0.92             | 49.90 p                         | 13.07 p                     | ±3.18                        | Polymer dielectric        |
| $C_{\alpha_4}$ | P2          | 8              | 6.43        | -82.59              | 0.92             | 47.52 p                         | 12.37 p                     | ±3.12                        | Polymer dielectric        |
| $C_{\alpha_5}$ | G2          | 1              | 1.64        | -55.68              | 0.62             | 24.74 n                         | 48.50 p                     | ±3.15                        | rGO-Polymer Composite      |

solutions are poured onto the dissolved PVDF-TrFE-CFE (two vials) polymer solution and mixed under continuous stirring for another 24 hours. In total, three different polymer and composite solutions labeled TP2, P2, and G2 are prepared. Au-covered, 2 cm × 2 cm Si/SiO$_2$ wafers are used to fabricate the FoC by drop-casting the composite solution. A 10 nm Ti layer followed by 190 nm Au layer is deposited on Si/SiO$_2$ wafers via DC sputter to define the bottom electrode. The composite solutions are drop-cast and dried for 12 hours at 85 °C under a vacuum. The other Au circular form electrodes of 3 mm diameter and 200 nm thickness are deposited in a similar way, using a shadow mask. Finally, nine samples of FoCs of the same order are flip-bonded on a printed circuit board and each capacitor gives a separate connection for the electrical measurements. The photo of an example of the fabricated G2 device, including a cross-sectional SEM image of rGO nanosheets/PVDF-TrFE-CFE nanocomposite, is shown in Fig. 3.

The behavior of three types (TP2, P2, G2) of fabricated FoCs of different orders (respectively $\alpha = 0.69$, 0.92, 0.62) was verified using the Agilent 4294A precision Impedance Analyzer. A photograph of the experimental workstation with fabricated solid-state G2 device is given in Fig. 4. During the experimental validation in the frequency range of our interest, 0.2 MHz – 20 MHz (801 logarithmically spaced points), a sinusoidal input signal with a default AC voltage of 500 mV and a frequency of 1 MHz was applied, while the common node was grounded ($V_g = 0$ V). Standard calibration tests (open and short circuits) of the 16047E Test Fixture were performed to calibrate the instrument. The measurement results are summarized in Table 3. Here, the magnitude, phase angle, i.e. FoC order, pseudo-capacitance, and equivalent integer-order capacitance at center frequency $f_c = 2$ MHz of the corresponding pins of all the devices are provided. From the results, a slight difference in the pseudo-capacitance values within the same device can be observed, which, however, gives us the flexibility to use different values within the same chip. On the other hand, the relative phase error at $f_c$ is significantly low. In addition, the measured phase angle deviation in two decades of the frequency range of our interest is only ±4 degrees ($|\text{max} - \text{min}|/2$). Therefore, the main benefits of these fabricated devices against the above-discussed practical FoC realizations or emulators [32], [35]–[40] are that they demonstrate lower phase angle deviation, better tuning on phase angle, wider bandwidth, and reproducible results.

B. ARBITRARY-ORDER FoC CONNECTIONS

To validate the introduced theory, the series- and parallel-connected arbitrary-order FoC structures depicted in Figs. 1 and 2 and their selected interconnections were verified via experimental measurements using fabricated solid-state passive FoCs introduced above. The experimental setup described in Section III A was used. The magnitude and phase responses of equivalent FoCs obtained using the Agilent 4294A precision Impedance Analyzer were proved by theoretical calculations (values given in paren-
TABLE 4. Results of arbitrary-order series-connected two and three FoCs: Measurement (calculated via MATLAB code).

<table>
<thead>
<tr>
<th>No.</th>
<th>#1</th>
<th>#2</th>
<th>#3</th>
<th>#4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Connection of FoCs</td>
<td>$C_{\alpha_1} + C_{\alpha_4}$</td>
<td>$C_{\alpha_1} + C_{\alpha_5}$</td>
<td>$C_{\alpha_4} + C_{\alpha_5}$</td>
<td>$C_{\alpha_1} + C_{\alpha_4} + C_{\alpha_5}$</td>
</tr>
<tr>
<td>Connection of Orders</td>
<td>0.69 + 0.92</td>
<td>0.69 + 0.62</td>
<td>0.92 + 0.62</td>
<td>0.69 + 0.92 + 0.62</td>
</tr>
<tr>
<td>Equivalent Impedance @ $f_c$ [kΩ]</td>
<td>8.72 (8.51)</td>
<td>4.00 (3.82)</td>
<td>7.84 (7.73)</td>
<td>10.22 (9.83)</td>
</tr>
<tr>
<td>Phase [°]</td>
<td>$-76.11 (-77.31)$</td>
<td>$-58.19 (-59.73)$</td>
<td>$-76.81 (-77.57)$</td>
<td>$-72.49 (-74.03)$</td>
</tr>
<tr>
<td>Relative Phase Error [%]</td>
<td>$-1.55$</td>
<td>$-2.58$</td>
<td>$-0.98$</td>
<td>$-2.08$</td>
</tr>
<tr>
<td>Equivalent Order $\alpha$ [-]</td>
<td>0.85 (0.86)</td>
<td>0.65 (0.66)</td>
<td>0.85 (0.86)</td>
<td>0.81 (0.82)</td>
</tr>
<tr>
<td>Pseudo-Capacitance [Farad·sec$^{-\alpha}$]</td>
<td>113.78 (93.72) p</td>
<td>6.42 (5.14) n</td>
<td>111.33 (98.47) p</td>
<td>187.33 (148.03) p</td>
</tr>
</tbody>
</table>

TABLE 5. Results of arbitrary-order parallel-connected two and three FoCs: Measurement (calculated via MATLAB code).

<table>
<thead>
<tr>
<th>No.</th>
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<th>#6</th>
<th>#7</th>
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<tr>
<td>Connection of FoCs</td>
<td>$C_{\alpha_4}$</td>
<td></td>
<td>$C_{\alpha_5}$</td>
<td>$C_{\alpha_4}$</td>
</tr>
<tr>
<td>Connection of Orders</td>
<td>0.69</td>
<td></td>
<td>0.62</td>
<td>0.69</td>
</tr>
<tr>
<td>Equivalent Impedance @ $f_c$ [kΩ]</td>
<td>1.67 (1.72)</td>
<td>0.934 (0.934)</td>
<td>1.28 (1.27)</td>
<td>0.835 (0.812)</td>
</tr>
<tr>
<td>Phase [°]</td>
<td>$-67.03 (-67.66)$</td>
<td>$-58.03 (-58.36)$</td>
<td>$-61.16 (-61.10)$</td>
<td>$-59.93 (-61.46)$</td>
</tr>
<tr>
<td>Relative Phase Error [%]</td>
<td>$-9.93$</td>
<td>$-0.57$</td>
<td>0.10</td>
<td>$-2.65$</td>
</tr>
<tr>
<td>Equivalent Order $\alpha$ [-]</td>
<td>0.74 (0.75)</td>
<td>0.64 (0.65)</td>
<td>0.68 (0.68)</td>
<td>0.66 (0.68)</td>
</tr>
<tr>
<td>Pseudo-Capacitance [Farad·sec$^{-\alpha}$]</td>
<td>3.10 (2.68) n</td>
<td>28.34 (26.67) n</td>
<td>11.66 (11.89) n</td>
<td>22.84 (17.42) n</td>
</tr>
</tbody>
</table>

TABLE 6. Results of interconnected (series-parallel) arbitrary-order FoCs: Measurement (calculated via MATLAB code).

<table>
<thead>
<tr>
<th>No.</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Connection of FoCs</td>
<td>$(C_{\alpha_4} + C_{\alpha_5})$</td>
<td></td>
</tr>
<tr>
<td>Connection of Orders</td>
<td>$(0.69 + 0.69)</td>
<td></td>
</tr>
<tr>
<td>Equivalent Impedance @ $f_c$ [kΩ]</td>
<td>4.33 (4.26)</td>
<td>1.91 (1.39)</td>
</tr>
<tr>
<td>Phase [°]</td>
<td>$-64.09 (-65.58)$</td>
<td>$-68.68 (-69.27)$</td>
</tr>
<tr>
<td>Relative Phase Error [%]</td>
<td>$-2.27$</td>
<td>$-0.85$</td>
</tr>
<tr>
<td>Equivalent Order $\alpha$ [-]</td>
<td>0.71 (0.73)</td>
<td>0.76 (0.77)</td>
</tr>
<tr>
<td>Pseudo-Capacitance [Farad·sec$^{-\alpha}$]</td>
<td>2.04 (1.58) n</td>
<td>2.01 (2.47) n</td>
</tr>
</tbody>
</table>

FIGURE 5. Two and three arbitrary-order FoCs connected in series: (a) magnitude, (b) phase responses.

The results obtained, including each individual FoC, are shown in Figs. 5(a) (magnitude) and (b) (phase), while the comparison of measured values at $f_c = 2$ MHz and calculated results is evaluated in Table 4. To estimate the equivalent order $\alpha$ (or phase), the magnitude data measured are fitted to the function $\log|Z| = a \log f + \log(2\pi f^0 C_{\alpha})$ using the linear least squares (LLS) method. Note that the magnitude responses are given in the logarithmic scale, while the phase responses are in linear scale. The equivalent equations from fitting the magnitude or phase as obtained from measurement

theses in Tables 4–6) via the MATLAB open access source code given in Appendix. In order to compare the FoCs, the fundamental orders (the Warburg pseudo- and integer-order ideal capacitance are set as 790 nFarad·sec$^{-0.5}$ and 158 pF, respectively) are plotted in all Figures by means of magnitude and phase.

1) SERIES CONNECTION OF ARBITRARY-ORDER FoCs
Firstly, the magnitude and phase responses of two and three arbitrary-order series-connected FoCs were studied.
samples are provided inside Fig. 5. From the results the orders are evident of FoCs as single devices TP2, P2, G2, i.e. 0.69, 0.92, and 0.62, with corresponding phases $-61.91$, $-82.59$, and $-55.68$ [degrees], while their equivalent orders from series connections are found to be 0.85, 0.65, 0.85, 0.81 (corresponding to Table 4 cases #1 $\rightarrow$ #4 with phases $-76.11$, $-58.19$, $-76.81$, $-72.49$). The equivalent magnitudes vary in ranges of (67.2 $\rightarrow$ 1.26, 17.87 $\rightarrow$ 0.829, 61.69 $\rightarrow$ 1.16, and 72.38 $\rightarrow$ 1.69) k$\Omega$ for cases #1 $\rightarrow$ #4, respectively. Via experiments we also demonstrated that the phase can be tuned by connecting different orders as depicted in Fig. 5(b). Furthermore, Table 4 gives the corresponding pseudo capacitances and relative phase errors of measured phases relative to the calculated values, which are at $f_c$ in the range of $-2.58\%$ to $-0.98\%$.

2) PARALLEL CONNECTION OF ARBITRARY-ORDER FoCs

Secondly, the behavior of two and three arbitrary-order FoCs connected in parallel were experimentally verified. The magnitude and phase responses of the equivalent impedances are shown in Figs. 6(a) and (b), respectively, while a comparison of the values measured at $f_c$ and the results calculated via the MATLAB open access source code are listed in Table 5. The equivalent new orders, which are achieved using the LLS fitting and given in Fig. 6(a) next to the legend, are found to be 0.74, 0.64, 0.68, and 0.66. As can be observed, the orders match well to those obtained from the measured phase responses, which are depicted in Fig. 6(b). Overall, the equivalent impedances have capacitive behavior and vary in ranges of (9.24 $\rightarrow$ 0.27) k$\Omega$, (4.11 $\rightarrow$ 0.19) k$\Omega$, (5.84 $\rightarrow$ 0.24) k$\Omega$, and (3.78 $\rightarrow$ 0.16) k$\Omega$ for cases #5 $\rightarrow$ #8, respectively. It is also worth noting that the relative phase errors at $f_c$ are again small and vary in the range of $-2.65\%$ to $0.10\%$.

3) SERIES-PARALLEL INTERCONNECTION OF ARBITRARY-ORDER FoCs

Finally, two selected series-parallel interconnections of FoCs were evaluated. The equivalent impedance of the first topology from Fig. 7 can be expressed as:

$$Z_{eq,#9}(s) = \frac{3\alpha_1 C_{a1} + 2\alpha_2 C_{a4}}{s^{\alpha_1} + s^{\alpha_2} C_{a1} C_{a2} + 2s^{\alpha_1 + \alpha_2} C_{a4} C_{a5}}, [\Omega]$$

(23)

where $\alpha_1 \equiv \alpha_2 \neq \alpha_3 \neq \alpha_4$ and $C_{a1} \equiv C_{a2} \neq C_{a3} \neq C_{a4}$. Similarly, the equivalent impedance of the structure given in Fig. 8 can be found as:

$$Z_{eq,#10}(s) = \frac{2}{2\alpha_1 C_{a1} + 3s^{\alpha_1} C_{a5}}, [\Omega]$$

(24)

while $\alpha_1 \equiv \alpha_2 \neq \alpha_3 \equiv \alpha_4$ and $C_{a1} \equiv C_{a2} \neq C_{a3} \neq C_{a4}$.

Here it is important to note that this is the very first attempt in the literature to calculate and measure the equivalent magnitude and phase of the arbitrary-order interconnected FoCs. A detailed comparison of the results at $f_c$ is given in Table 6 and depicted in Fig. 9. The equivalent orders of interconnections #9 and #10 obtained using the LLS fitting are 0.71 and 0.76, which correspond to the phases $-64.09$ and $-68.68$ [degrees], respectively. The calculated relative phase errors are respectively $-2.27\%$ and $-0.85\%$ for the first and second topology, which are very favorable results. Overall, from the results obtained it is clear that the measurement results are in very good agreement with theoretically predicted ones.
IV. BRIEF DISCUSSION OF RESULTS

Figures 10(a) and (b) give a comparison of the calculated, measured, and fitted line values of the magnitude and phase responses of three arbitrary-order series- and parallel-connected FoCs (cases #4 and #8 of Tables 4 and 5, respectively). It is evident that the results calculated using the MATLAB open access source code match well with the fitted values and the measured values. Furthermore, the equivalent pseudo-capacitance versus frequency is plotted for both circuits in Fig. 10(c). As can be observed, the pseudo-capacitance of both FoCs is constant in the same region as the phase is. The normalized histograms show low absolute error between the measured and the calculated equivalent integer-order capacitance values, which is less than 1 pF and 4 pF, respectively, for the series- and parallel-connected FoCs.

Evaluating in brief the obtained results it can be concluded that the equivalent impedances of fabricated arbitrary-order FoCs connected in series and parallel exhibit the same capacitive behavior as integer-order capacitors. Despite the claim in [32], here it is important to underline that the phase responses of series- and parallel-connected FoCs of arbitrary orders are constant. The experimental results are in good agreement with theory and calculated results. Note that the accuracy of the above theoretical analyses is proved and the proposed approach offers flexibility and a degree of freedom to work with any order of FoCs with random connection.

V. CONCLUSION

The paper presented a novel analytical approach of series- and parallel-connected \( n \) arbitrary-order FoCs. Particularly, as practical case studies, two and three arbitrary-order FoCs were used in series- and parallel-connected circuits and the magnitude and phase responses, i.e. the order, of the equivalent impedances were evaluated in detail. In addition, the units of these physical dimensions were discussed. More-
Open access source code 1. MATLAB code for calculating equivalent FoCs.

```matlab
% MATLAB code for calculating the equivalent impedance of ... 
% n FoCs and plotting the magnitude and phase responses 
% Copyright (c) 2018, A. Kartci, A. Agambayev, N. Herencsar, and K.N. Salama 
% Brno University of Technology & King Abdullah University ... 
% Science and Technology 
% All rights reserved. 
% Feel free to use/modify these codes as you see fit. Any ... 
% publications codes, papers, technical reports, etc. ... 
% in which you code (in their original or a modified ... 

% Set the order and fractional-order capacitance value 
order = [0.69 0.92 0.62]; 
Fc = [5.52e9 47.52e12 26.74e9]; 

% Calculating the equivalent impedances 
for n = 1:length(order) 
    Z1 = 1/(s/order(n)+Fc(n)); 
    pretty(Z1); 
end

% Equivalent impedance of series connection 
Zseries = sum(Z1); 
Yseries = (-s*order(n)+Fc(n)); 
pretty(Yseries); 
Zseries = sum(Yseries); 

% Equivalent impedance of parallel connection 
Zparallel = 1/Zseries; 

% Plotting the results for series-connected FoCs 
num = eval(Zseries); 
z = [z1_zeros(num)]; 
z2 = z/2*exp(s); 
f = linspace(0.01,10000); 
t = size(f); 
for n = 1:n+1 
    Zmag(n) = abs(num,exp(s))./s+2*pi*n); 
end

% Phase of equivalent impedance (degree) 

% Plotting the results for parallel-connected FoCs 
num = eval(Zparallel); 
z1 = [z1_zeros(num)]; 
z2 = z/2*exp(s); 
f = linspace(0.01,10000); 
t = size(f); 
for n = 1:n+1 
    Zmag(n) = abs(num,exp(s))./s+2*pi*n); 
end

% Phase of equivalent impedance (degree) 

% Plotting the results for series-connected FoCs 
num = eval(Zseries); 
z1 = [z1_zeros(num)]; 
z2 = z/2*exp(s); 
f = linspace(0.01,10000); 
t = size(f); 
for n = 1:n+1 
    Zmag(n) = abs(num,exp(s))./s+2*pi*n); 
end

% Phase of equivalent impedance (degree) 

% Plotting the results for parallel-connected FoCs 
num = eval(Zparallel); 
z1 = [z1_zeros(num)]; 
z2 = z/2*exp(s); 
f = linspace(0.01,10000); 
t = size(f); 
for n = 1:n+1 
    Zmag(n) = abs(num,exp(s))./s+2*pi*n); 
end

% Phase of equivalent impedance (degree) 
```

References


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