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RESISTOR-LESS SINGLE-PURPOSE OR RECONFIGURABLE BIQUADS UTILIZING SINGLE Z-COPY CONTROLLED-GAIN VOLTAGE DIFFERENCING CURRENT CONVEYOR

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Presented work deals with applications of the single Z-Copy Controlled-Gain Voltage Differencing Current Conveyor (ZC-CG-VDCC) in single purpose and multifunctional biquadratic voltage- and current-mode active resistor-less filters. Electronically adjustable features of the active device (intrinsic current input resistance R_x , current gain B and transconductance g_m) are controlled by DC bias current. These mentioned adjustable features allow interesting possibilities of control of the pole frequency and quality factor. Four voltage-mode solutions realizing low-pass, band-pass, high-pass and band-reject filtering solutions were designed together with two current-mode filters. The current-mode solutions have curious multifunctional capability based on full utilization of electronically controllable parameters of the ZC-CG-VDCC. Two adjustable parameters of the ZC-CG-VDCC (intrinsic current input resistance and transconductance) are used for electronic setting of features of the filter (pole frequency, quality factor, tuning). Control of the third adjustable parameter (current gain) causes reconnection-less change of the transfer function between iAP (inverting all-pass response) and iBR (inverting band-reject responses). Simulation results in PSPICE and Cadence CDS 6 are used to show characteristics of the proposed circuits.

Keywords: Biquads; current gain; electronic control; intrinsic resistance; reconnection-less multifunctional filter; transconductance; z-copy; ZC-CG-VDCC.

1. Introduction

Biquadratic active filters are very typical applications in analog signal processing [1]. Many approaches and solutions are known in literature, for instance [2, 3]. It is not possible to discuss all known solution in this contribution, therefore important examples were selected for further explanation.

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The most requested solutions are so-called multifunctional biquads [4] based on Kerwin, Huelsman and Newcomb (KHN) types [5]. These filtering structures provide many transfer functions simultaneously (low-pass - LP, band-pass - BP, high-pass - HP, band reject - BR and all-pass - AP). Basic and very favorable solutions are utilizing operational transconductance amplifiers (OTAs) in several variants [6]. Many various solutions based on OTAs were described and discussed in literature (for example [7-9]). Multifunctionality and various possibilities of electronic control of application usually require high number of these simple active devices in filtering application. On the other hand, very simple solutions utilizing only non-controllable current- or voltage-followers are also available (examples are in [10-12], also in frame of current conveyors [13]. However, electronically controllable features of the application are usually very restricted.

Another approaches to the synthesis consider combination of several different types of active elements in filtering application [14].

Similarly, advanced types of current conveyor (CC) [15] have controllable parameter known as intrinsic current input resistance (R_x) [16] that is also very useful in control of active filters [17]. They found employment in active devices, which combines several sub-blocks in internal topology. Typical example is so-called current differencing transconductance amplifier (CDTA) [4, 18] where current differencing unit and OTA are combined together [18, 19]. Similar approach was used in construction of so-called current conveyor transconductance amplifier (CCTA) [20] and [21] for example or current follower transconductance amplifier (CFTA) [22]. Mentioned active elements allow to combine both above discussed types of electronic control (g_m, R_x) in frame of one active device that improves possibilities of direct electronic control in applications. CDTA has modification, known as modified current differencing transconductance amplifier (MCDTA) [23]. Its parameters, input resistance, z terminal resistance and transconductance are electrically reconfigurable. Current-mode active elements with controllable current gain (B) were introduced in [24, 25]. Therefore, it is possible to find also active devices where various combinations of parameters (g_m, R_x, B) are independently and electronically adjustable. For example Minaei et al. [26] and Kumngern et al. [27] showed implementation of R_x and B control and Marcellis et al. [28] utilized adjustable current gain and voltage gain control in frame of active device [29].

Resistor-less filters have typical feature - no external resistors are required for their operation. OTAs allow such design [7-9] but some features are not available because OTA has only one electronically controllable parameter (transconductance - g_m). The latest literature [30-32] shows that resistor-less filtering applications are very popular. Some from latest works dealing with single active element based filters do not allow any possibility of electronic control [33], require external resistors [33, 34] or proper outputs of the responses are not available [33]. In addition, change of the input or output node (terminal) for change of the transfer function is required in these solutions.

Filtering applications, proposed in this contribution, use active device, that combines three types (g_m, R_x, B) of electronically and independently controllable parameters and

therefore allows multiple control of application. This active device was derived from basic variant of the voltage differencing current conveyor (VDCC) [4] and is called zcopy controlled-gain voltage differencing current conveyor (ZC-CG-VDCC). Behavioral model is shown in Fig. 1. This paper is focused on electronically controllable resistor-less biquadratic (2nd order) single ZC-CG-VDCC-based filters and shows how to construct voltage-mode solutions providing all basic 2nd order filtering functions with ZC-CG-VDCC and two current-mode filters where one type has very unusual, interesting and useful controllable feature (allows reconnection-less change of type of the transfer function).

Controllable features, that ZC-CG-VDCC offers, are not common in previous works. One known solution was published in [35], where the same type of reconnection-less change of the transfer function is available. Unfortunately, the quality factor of the circuit in [35] was restricted to 0.5. In comparison to [35], this solution allows to obtain higher quality factor, theoretically limited only by available range of $g_{\rm m}$ and $R_{\rm x}$ control.

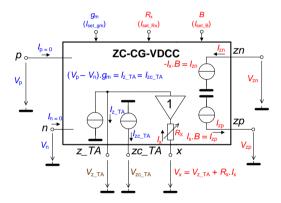


Fig. 1 Behavioral model of the z-copy controlled-gain voltage differencing current conveyor (ZC-CG-VDCC)

The parameters of the active elements in Fig. 1 (g_m, R_x, B) are controlled by DC bias currents. Voltage difference (Vp - Vn) at high-impedance input terminals is transformed to current I_{z_TA} through g_m of the OTA subsection. Identical copy of the I_{z_TA} is available at the high-impedance terminal zc_TA . Voltage V_X (low-impedance current input terminal X) is given by R_X (intrinsic resistance) and V_{z_TA} . Current through X terminal, $I_X = (V_{X^TA})$ $V_{z_{-}TA})/R_x$, is available at high-impedance current output terminals z_p , z_n (positive and negative) in amplified (by adjustable gain B) form. Detailed information are in [36].

2. Proposed Filtering Applications

The ZC-CG-VDCC elements can be easily applied in various filtering applications based on many standard synthesis and design approaches [4, 5, 7-9]. Even single ZC-CG-VDCC is suitable for synthesis and construction of biquadratic voltage- and current-mode filters with some partial benefits. Absence of external resistors is typical feature of proposed structures. Following text discuss several single purpose (one transfer function) and multifunctional filtering applications and their possibilities. Finally, one of presented variants has reconnection-less and electronically reconfigurable properties that is very beneficial and useful.

2.1. Voltage-mode low-pass filter

The circuit in Fig. 2 has one grounded and one floating capacitor. Transfer function of the circuit has form:

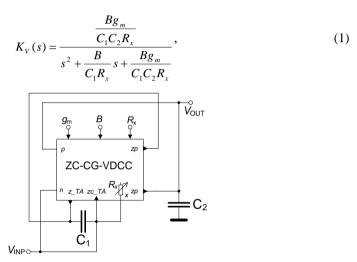


Fig. 2 Voltage-mode low-pass structure using ZC-CG-VDCC and two capacitors (one of them floating) where pole frequency and quality factor are given as:

$$\omega_p = \sqrt{\frac{g_m B}{C_1 C_2 R_x}}, Q_p = \sqrt{\frac{g_m R_x C_1}{B C_2}}$$
 (2), (3)

Interesting feature of this configuration (for B = 1) is that it allows control of pole frequency by g_m and R_x simultaneously whereas their product $g_m.R_x$ designates quality factor of the filter (their ratio has to be kept constant while the pole frequency is tuned).

2.2. Voltage-mode band-pass filter

The band-pass filter in Fig. 3 has both capacitors grounded, that is significant advantage for IC design. Transfer function has very similar form as (1):

$$K_{V}(s) = \frac{\frac{B}{C_{1}R_{x}}s}{s^{2} + \frac{B}{C_{2}R_{x}}s + \frac{Bg_{m}}{C_{1}C_{2}R_{x}}},$$
(4)

where pole frequency has the same form as (2) and quality factor is:

$$Q_p = \sqrt{\frac{g_m R_x C_2}{BC_1}} \tag{5}$$

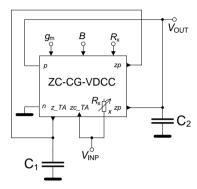


Fig. 3 Voltage-mode band-pass structure using ZC-CG-VDCC and two capacitors (both grounded)

2.3. Voltage-mode high-pass filter

The second-order high-pass filtering response is provided by structure shown in Fig. 4.

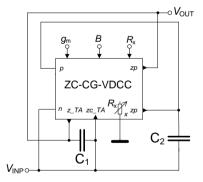


Fig. 4 Voltage-mode high-pass structure using ZC-CG-VDCC and two capacitors (both floating) Transfer function of the filter is as follows:

$$K_{V}(s) = \frac{s^{2}}{s^{2} + \frac{B}{C_{1}R_{x}}s + \frac{Bg_{m}}{C_{1}C_{2}R_{x}}},$$
(6)

and its pole (quality factor) is the same as (2) and (3).

2.4. Voltage-mode band-reject filter

The structure in Fig. 5 provides band-reject response as is obvious from the equation:

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$$K_{V}(s) = \frac{s^{2} + \frac{B(C_{1} - C_{2})}{C_{1}C_{2}R_{x}}s + \frac{Bg_{m}}{C_{1}C_{2}R_{x}}}{s^{2} + \frac{B}{C_{1}R_{x}}s + \frac{Bg_{m}}{C_{1}C_{2}R_{x}}}$$
(7)

Filter has band-reject characteristic if equality of both capacitor $(C_1 = C_2)$ is ensured. Practically, structure in Fig. 5 is identical to Fig. 4, only output node is different. Pole frequency and quality factor are also identical to (2) and (3).

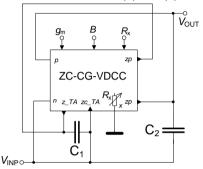


Fig. 5 Voltage-mode band-reject structure using ZC-CG-VDCC and two capacitors (both floating)

2.5. Current-mode band-pass filter

Band-pass filters are very important in many various applications in communication systems in both voltage- and current-mode. Therefore, we proposed also current-mode solution employing single ZC-CG-VDCC (Fig. 6).

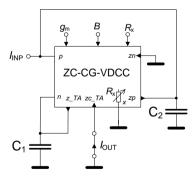


Fig. 6 Current-mode band-pass structure using ZC-CG-VDCC and two capacitors (both grounded)

Transfer function of the circuit has form:

$$K_{I}(s) = \frac{\frac{g_{m}}{C_{2}}s}{s^{2} + \frac{g_{m}}{C_{1}}s + \frac{g_{m}}{C_{1}C_{2}R_{*}}},$$
(8)

where pole frequency and quality factor are:

$$\omega_p = \sqrt{\frac{g_m}{C_1 C_2 R_x}}, Q_p = \sqrt{\frac{C_1}{g_m R_x C_2}}$$
 (9), (10)

2.6. Current-mode inverting all-pass and band-reject filter

Very interesting solution of the multifunctional structure is shown in Fig. 7. The filter has reconnection-less reconfigurable character, that is not typical feature in known solutions. All controllable features of the ZC-CG-VDCC are fully utilized to control parameters of the filtering structure.

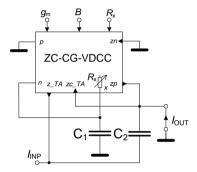


Fig. 7 Current-mode all-pass and band-reject electronically reconfigurable reconnection-less structure using ZC-CG-VDCC and two capacitors (one floating)

Transfer function can be found as very beneficial for controllable filters:

$$K_{I}(s) = -\frac{s^{2} + \frac{(C_{2} - C_{1}B)}{C_{1}C_{2}R_{x}}s + \frac{g_{m}}{C_{1}C_{2}R_{x}}}{s^{2} + \frac{1}{C_{1}R_{x}}s + \frac{g_{m}}{C_{1}C_{2}R_{x}}},$$
(11)

where inverting all-pass response is available for B = 2 (if $C_1 = C_2$) and inverting bandreject solution for B = 1. Pole frequency has the same form as (9) but the quality factor is different:

$$Q_p = \sqrt{g_m R_x \frac{C_1}{C_2}} {12}$$

This transfer function allows change of the filter response between two types of transfer (iAP and iBR). The third function of the structure allows direct inverting transfer (iDT) between input and output for B = 0 that can be useful in some cases at IC, where any other type of reconfigurability (reconnection of input, output, ...) without electronic control is problematic.

3. Verification of Theoretical Presumptions - Simulation results

We selected solution in Fig. 7 for detailed analysis and simulation in Cadence OrCAD (PSpice) with CMOS model of the ZC-CG-VDCC presented in [36] in detail. We suppose equality of $C_1 = C_2 = C = 100 \text{ pF}$ for easy application of the transfer type control by parameter B. Both electronically controllable parameters g_m and R_x are calculated from (9) and (12) as follows:

$$g_m = \omega_p Q_p C, R_x = \frac{Q_p^2}{g_m}.$$
 (13), (14)

Resulting parameters can be found as $g_{\rm m} = 1.88$ mS and $R_{\rm x} = 4.77~{\rm k}\Omega$ for pole frequency $f_p = 1$ MHz and quality factor $Q_p = 3$. DC bias control currents of the CMOS structure [36] are set to $I_{\text{set_gm}} = 145 \,\mu\text{A}$ and $I_{\text{set_Rx}} = 4.9 \,\mu\text{A}$. Results of the simulation for B=1 and B=2 are in Fig. 8. The complex space plot of polynomial numerator and denominator roots (zeros and poles) is depicted in Fig. 9. Obtained f_p was 993 kHz. Supply voltage was $V_{DD} = V_{SS} = \pm 1 \text{ V}$ and power consumption is below 7.4 mW.

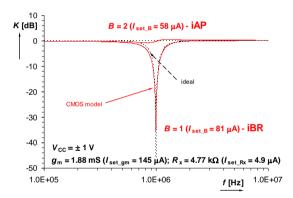


Fig. 8 Magnitude responses of the filter for two values of current gain B (electronic change of the transfer type between iBR and iAP response)

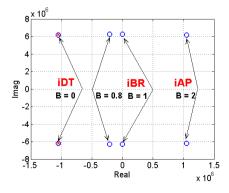


Fig. 9 Complex space plot of the zeros migration of the filter

Change of the stop-band gain (attenuation) is possible by B in range <0; 1> as Fig. 10 shows (tested B values are 0.5, 0.8 and 1). Phase responses are documented in Fig. 11.

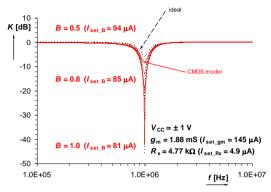


Fig. 10 Magnitude responses of the iBR filter for B between 0.5 and 1

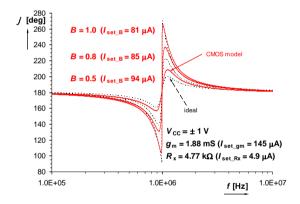


Fig. 11 Phase responses of the iBR filter for B between 0.5 and 1

Maximal theoretical attenuation (minimal gain) at the pole frequency (f_p, ω_p) is given by:

$$\left| K_{\min} \left(\omega_p \right) \right| = \frac{C_2 - C_1 B}{C_2}, \tag{15}$$

where, supposing $C_1 = C_2 = C$, we can found simplified form: $|K_{\min}| = 1 - B$. In logarithmic range:

$$K_{\min}[dB] = 20 \log_{10}(1-B).$$
 (16)

Resulting magnitude responses for higher gains (B = 1.5, 1.8, 2 and 3) are shown in Fig. 12.

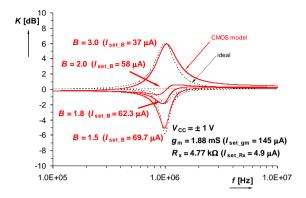


Fig. 12 Magnitude response of the filter for B between 1.5 and 3

Table 1	Ideal	and s	simulated	results	(Pole	Freque	ncy Tuning)
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g_{m}	$f_{ m p}$	$f_{ m p}$	BW	BW	$Q_{\rm p}$	$Q_{ m p}$
[mS]	ideal	simulated	ideal	simulated	ideal	simulated
$I_{ m set_gm}$	[kHz]	[kHz]	[kHz]	[kHz]	[-]	[-]
[µA]						
0.15	282	278		335	0.84	0.83
(5.5)						
0.25	364	363		338	1.09	1.07
(9.9)						
0.5	515	511	335	345	1.54	1.48
(22)						
1.0	729	724		360	2.18	2.01
(54)						
1.88	1 000	993		385	2.99	2.58
(145)						

Configurations of the iBR filter for two values of quality factor Q_p (0.74 and 2.58) were tested and results are shown in Fig. 13. Simultaneous reconfiguration of g_m and R_x is required but electronic change of both parameters is very simple.

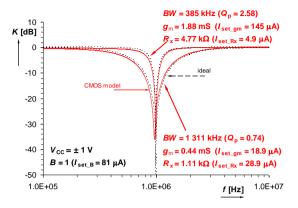


Fig. 13 Magnitude response of the iBR filter for two different values of Q_p at $f_p = 1$ MHz

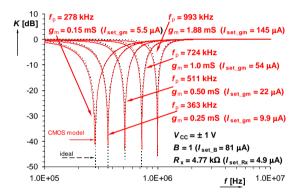


Fig. 14 Tuning of the iBR response with constant BW

Tuning of the iBR response is shown in Fig. 14 for adjusting of $g_{\rm m}$ in range from 0.15 to 1.88 mS ($I_{\rm set_gm} = 5.5\text{-}145 \,\mu\text{A}$). Adjusting of $g_{\rm m}$ only ensures $f_{\rm p}$ tuning without change of the bandwidth (BW) theoretically, see Table 1.

Configuration of the iAP filter (B=2) was studied while f_p was tuned (g_m values: 0.15, 0.25, 0.5, 1.0, 1.88 mS, the same values as in Tab. 1) and constant $Q_p = 2.58$ (as in previous case for iBR). Results are shown in Fig. 15 and Fig. 16 (magnitude and phase responses). The pole frequencies obtained in simulation results are very close to results in Tab. 1. Details are given in both figures (Fig. 15, Fig. 16).

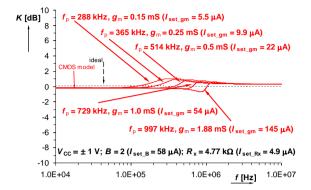


Fig. 15 Tuning of the iAP filter (magnitude responses)

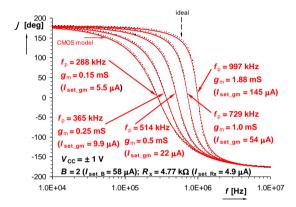


Fig. 16 Tuning of the iAP filter (phase responses)

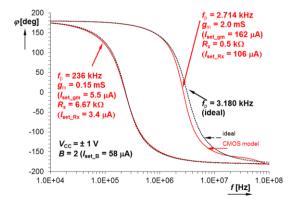


Fig. 17 Wide-range tuning of the iAP filter (phase responses) for two discrete values of $g_{\rm m} = R_{\rm x}$

The iAP response allows wide range of f_p when $Q_p = 1$ (when $g_m = R_x$). Range of f_p adjusting was verified from 236 kHz to 2.714 MHz ($g_m = 0.15$ and 2 mS), as is obvious from Fig. 17.

Monte Carlo and corner (incl. temperature effects) analyses in Cadence Virtuoso CDS 6 have been provided to see performance of the above discussed system (Fig. 7) in real conditions and under fabrication dispersion. Temperature was swept from -20 to 80 deg. of C for all corner runs. Monte Carlo (for 200 runs) and Corner analysis results are presented for Q = 0.7 and Q = 2.58 (all parameters in accordance to Fig. 13). An example of results for BR and setting Q = 2.58 is shown in Fig. 18 (Monte Carlo analysis) and Fig. 19 (corners and temperature effects). Corner analysis supposes about 60 possible combinations of temperature (-20, 0, 20, 80 deg.), four corner models for CMOS transistors and three models for capacitors.

Table 2 Process variation for AP configuration of the filter in	in Fig. '	7
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Table 2 1100000 variation for the configuration of the finer in Fig. /									
	Monte	Carlo	Corner (W	orst Case)					
	mean	max.							
	Q = 0.7 (setting)								
$f_{\rm p} [{ m kHz}]$	1 053	19.8	799	1 428					
$\Delta K [dB]^*$	1.55	0.25	1.34	2.83					
	Q = 2.58 (setting)								
$f_{\rm p} [{ m kHz}]$	1 033	22.9	766	1 429					
$\Delta K [dB]^*$	1.30	0.29	0.94	2.78					

Table 3 Process variation for BR configuration of the filter in Fig. 7

	Monte	Carlo	Corner (Worst Case)						
	mean	std. dev.	min.	max.					
Q = 0.7 (setting)									
f_{p} [kHz]	1 041	21.2	751	1 412					
K_{\min} [dB]	-24	4.7	-55	-4					
Q [-]	0.67	0.01	0.66	1.20					
	Q	Q = 2.58 (setting	g)						
f_{p} [kHz]	1 031	23.1	760	1 424					
K_{\min} [dB]	-35	7.9	-48	-5					
Q [-]	2.55	0.04	2.51	3.90					

^{*} ΔK – max. magnitude change from 0 dB at AP response

The worst case (corner) analysis results indicate significant effect on f_p accuracy, and stop-band attenuation (BR response). It is quite typical behavior for presented range of temperatures. However, corner analysis really offers the most pessimistic scenario that will be most likely far away from real situation. Moreover, any influence on stop-band

and f_p setting can be easily corrected by electronically adjustable parameters of the ZC-CG-VDCC (g_m, R_x, B) and therefore it is not so questionable.

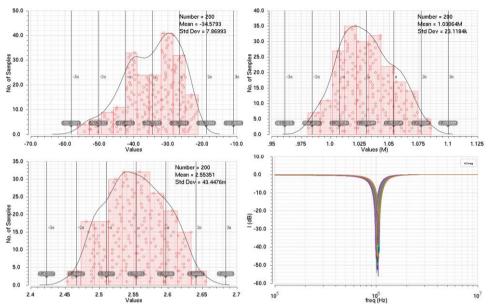


Fig. 18 Cadence Virtuoso CDS 6 Monte Carlo analysis results in case of BR response (from left side –

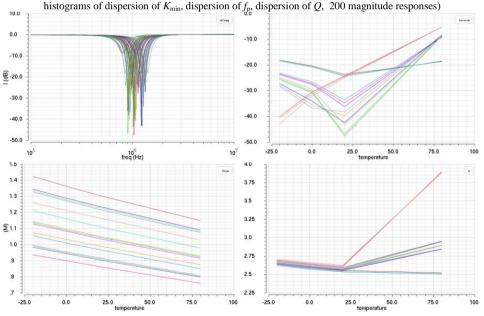


Fig. 19 Cadence Virtuoso CDS 6 Corner analysis results for BR response (from left side – magnitude responses for about 60 runs/combinations, dependence of K_{min} on corner groups and temperature, dep. of f_p on corner groups and temperature)

4. Proposed Solutions Versus State-of-the-art

ZC-CG-VDCC device has been utilized in structures regarding first-order filtering applications, see [36], [37]. Implementation of this device in second-order structures was not solved in the past.

In this work, ZC-CG-VDCC device was used in exemplary single-purpose second-order filtering structures that are available in frame of just one active device. To the best of author's knowledge no similar study, where ZC-CG-VDCC device has been used in design of second-order single purpose filters, is available in open literature. Therefore we prepared several voltage-mode and current-mode second-order filtering topologies (Fig. 2 - Fig. 6). Solutions serving for similar purposes based on another active devices (in many cases more than one) than ZC-CG-VDCC allow to establish similar transfer responses and can be found in open literature. We noted them in this paper only for completion of synthesis of second-order single purpose filters.

However, the structure in Fig. 7 belongs to reconfigurable second-order reconnection-less solutions that were also reported in literature several-times and significantly improve some features of reported circuits (complexity especially). First of all, to the best of author's knowledge, Fig. 7 is the first reported solution allowing electronic reconfiguration of the transfer function in frame of the single active devicebased biquad filter. Therefore, it is worth to provide precise comparison with recent knowledge and this structure was also analyzed in detail by simulations. Comparison of previously reported solutions of the second-order reconfigurable filters is provided in Tab. 4. Presented solution in Fig. 7 does not fulfill conditions for full universality (as we can see from the table it provides reconfiguration between three transfer functions), therefore there is still space for further improvements as we expect from our future work.

Table. 4 Comparison of previously reported reconnection-less reconfigurable filtering structures and solution presented in Fig. 7

	presented in Fig. 7									
References	No of passive/active elements	Floating capacitor	Type of active devices	Type of control	Available transfer functions	Controllable quality factor				
[35]	4/2	Yes	CCII	current gain	AP, BR	No				
[38]	7(4)/7(6)	No	CCII, CA, VB, CF, OTA	current gain transconductance	AP, BR	Yes				
[39]	2-3/4	Yes	OTA	transconductance	AP, BR, HP, LPZ, HPZ	Yes				
[40]	2/4	Yes	OTA	transconductance	AP, BR, HP, BP, LPZ, HPZ	Yes				
	•					·				
Fig. 7	2/1	Yes	ZC-CG-VDCC	transconductance,	AP, BR, DT	Yes				

There are some solutions [39], [40] allowing reconnection-less reconfigurability between various transfer types (band-pass, low-pass and high-pass with transfer zeros) as well as control of quality factor as solution presented in this paper. However, they require higher amount of active devices (see Tab. 4). In the class of the circuits providing reconfiguration between AP and BR response only, solution in [35] does not allow to control quality factor which is very low (0.5). Structure in [38], providing the same transfer responses, requires high number of passive and active elements. Therefore, circuit presented here overcomes several drawbacks of structures reported in previous works. Moreover, better controllability of reconfiguration of transfer function by current gain (B) than by transconductance (g_m) is expected due to simpler availability of zero value of B than zero value of g_m used in [39]-[40]. Solutions in [39] and [40] allows reconfigurability between higher number of transfer responses but number of required active devices is quite high and some transfer responses are not pure (BP with finite attenuation in the stop band for example).

In general point of view, single active device-based biquadratic structures reported in recent literature does not allow electronic reconfiguration between several transfer types as we can see from Tab. 5. There are drawbacks of many solutions regarding only limited and passive control (by R or C) of specific features [3], [33], [34], [41]-[44]. Therefore, electronic controllability of some solutions is very restricted or not allowed. The second problem is requirement of many passive elements, see for example [3], [41], [43]. Only solutions in [21] and [23] are offering the same or even better features than circuits in Fig. 2 – Fig. 6 but these does not allow direct transfer and reconnection-less electronical reconfiguration between several transfer functions as circuit in Fig. 7. So the main features and differences of the transfer type change approach of the circuit in Fig. 7 among others are evident, see Tab. 5.

Tab. 5 General comparison of previously reported examples of single active device-based biquad filters

Reference	Mode	Number of passive elements	Floating capacitor	Type of active device	Type of control	Generally available transfer functions	Change of topology	Change of input/output	Reconnection less type	Electronically controllable narameters	Electronically settable
[3]	voltage	8	Yes	CDBA	passive	LP, HP, BP, BR, AP	Yes	Yes	No	No	No
[21]	current	2	No	CCCTA	$R_{\rm x},g_{\rm m}$	LP, HP, BP, BR, AP	No	Yes	No	Yes	Yes
[21]	voltage	2	Yes	CCCTA	$R_{\rm x},g_{\rm m}$	LP, HP, BP, BR, AP	Yes	Yes	No	Yes	Yes
[23]	current	2	No	MCDTA	$R_{\rm x},g_{\rm m}$	LP, HP, BP	No	Yes	No	Yes	Yes
[33]	current	4	No	DVCC	passive	LP, HP, BR	No	Yes	No	No	N/A
[34]	voltage	4	Yes	CCTA	g _m , passive	LP, HP, BP, BR, AP	Yes	Yes	No	partially	N/A
[41]	both	5	No	FDCCII	passive	LP, HP, BP, BR, AP	No	Yes	No	No	N/A

LP, HP, BP, BR, AP

No Yes No

,				<u>, </u>	1						
[43]	current	5	Yes	CFOA	passive	LP, HP, BP, BR, AP	Yes	Yes	No	No	N/A
[44]	current	4	No	CBTA	g _m , passive	LP, HP, BP, BR, AP	No	Yes	No	No	N/A
[44]	voltage	4	No	CBTA	g _m , passive	LP, HP, BP, BR, AP	No	Yes	No	No	N/A
Fig. 2 – Fig. 5	voltage	2	Yes	ZC-CG-VDCC	$R_{\rm x},g_{ m m},$ B	HP, BP, BR	Yes	Yes	No	Yes	Yes
Fig. 7	current	2	Yes	ZC-CG-VDCC	$R_{\rm x},g_{ m m},$ B	AP, BR, DT	No	No	Yes	Yes	Yes

passive

FDCCII

Notes:

current

[42]

CA – current amplifier, CF – current follower, CCII – current conveyor of second generation, CDBA – current differencing buffered amplifier, CCCTA, current controlled current conveyor transconductance amplifier, MCDTA – multiple output current differencing transconductance amplifier, DVCC – differential voltage current conveyor, FDCCII – fully differential current conveyor of second generation, CFOA – current feedback, CBTA – current backward transconductance amplifier, OTA – operational transconductance amplifier, ZC-CG-VDCC – z-copy controlled gain voltage differencing current conveyor, VB- voltage buffer

5. Conclusion

We introduced several interesting solutions of the biquad filters in voltage- and current-mode based on new single active device so-called Z-Copy Controlled-Gain Voltage Differencing Current Conveyor (ZC-CG-VDCC). All solutions utilize external capacitors only (resistor-less) and electronically controllable parameters for configuration of the filter characteristics. Circuits in Fig. 2 (LP), Fig. 3 (BP) and Fig. 4 (HP) have different topologies. Figure 5 (BR) has the same topology as Fig. 4, but other output response is presented. Denominators of transfer functions of solutions from Fig. 2 - Fig. 5 are identical. Current-mode representation of the band -pass filter is shown in Fig. 6. Both grounded capacitors are an advantage of this circuit. Solution from Fig. 7 presents very useful features. This circuit is very favorable for its electronically reconfigurable features (in IC design especially). Electronically adjustable parameters of the ZC-CG-VDCC element are fully employed for electronic change of the transfer function (inverting all-pass and band-pass response) without reconnection of nodes (input/output) and electronic control of parameters of the filter. The last noted filter was selected for detained analysis based on PSpice and Cadence IC6 simulations with recently reported CMOS model of the ZC-CG-VDCC [36] and tested in range of pole frequency (f_p) 0.28 -0.99 MHz with quality factor Q_p equal to 2.58. Lower quality factor $Q_p = 1$ (equality $R_x = g_m$) allows wider range of f_p control (between 0.24 - 2.7 MHz) than single parameter control (constant bandwidth). Theoretical presumptions were confirmed by detailed analysis and simulation.

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